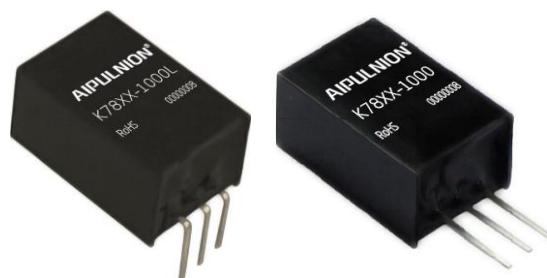




Typical Features

- ◆ Wide input voltage range, non-Isolated & regulated single output
- ◆ High Efficiency up to 97%
- ◆ Small compact SIP packing
- ◆ Short circuit, Over-heat Protection
- ◆ No external components needed
- ◆ Low ripple & Noise
- ◆ Operating Temperature: -40°C~+85°C
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25°C

Output Specifications

Item	Working Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	Full load	--	±2	±3	%
Ripple & Noise*	Nominal input, full load, 20MHZ bandwidth	--	25	40	mVp-p
Load Regulation	10% ~ 100% load	--	±0.4	±0.6	%
Line Regulation	Input Voltage Change	--	±0.2	±0.4	%
Temperature Drift Coefficient	100% Full Load	--	--	±0.03	%/°C
Over Heat Protection	IC build-in	--	150	--	°C
Output Short Circuit Protection		Continuous, Self-recovery			

NOTE: Ripple & Noise tested by twisted-pair method;

General Specifications

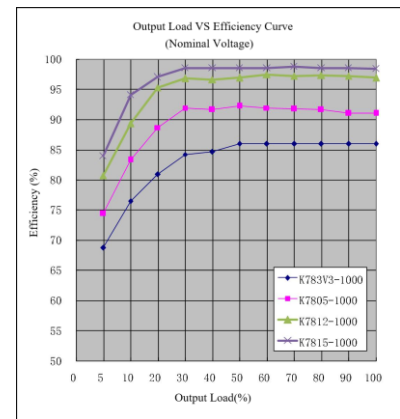
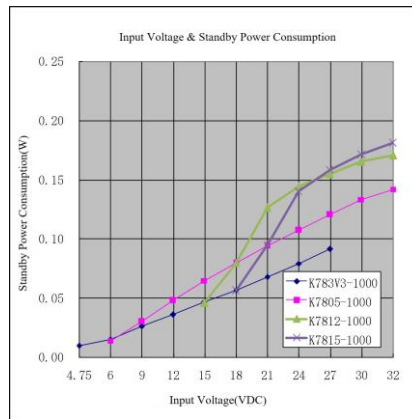
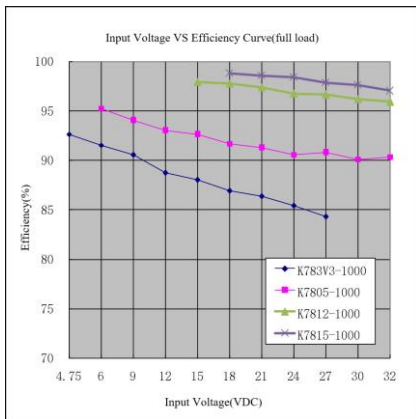
Switching Frequency	Typical	350KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40°C ~ +85°C
Storage Temperature		-55°C ~ +125°C
Shell temperature rise during work		100°C (MAX.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic (UL94 V-0)
Product Weight		4.0g (Typ.)
Pin Withstand Soldering Temp	Distance to case 1.5mm, 10S	300°C
MTBF	MIL-HDBK-217F@25°C	20X10 ⁵ Hrs



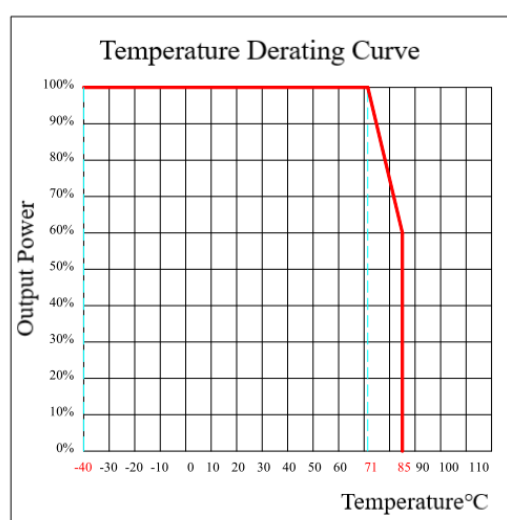
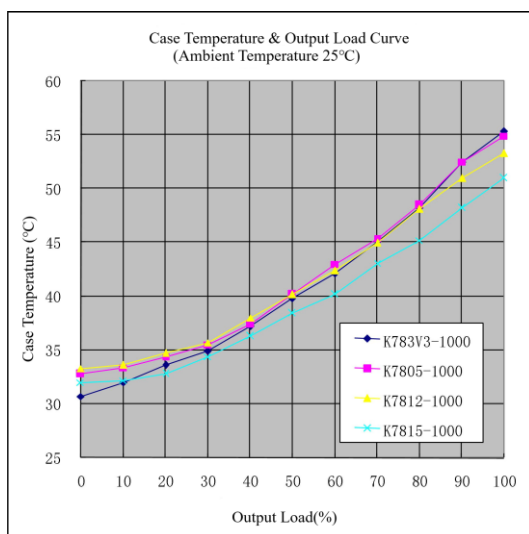
Typical Product List

Part No.	Input Voltage Range (VDC)	Output		Quiescent Current (mA) Typ.	Capacitive Load uF	Ripple & Noise mVp-p Typ.	Efficiency (%) (Typ.)	
		Voltage (VDC)	Current(mA) MAX.				Vin (Min)	Vin (max)
K783V3-1000(L)	24(4.75-28)	3.3	1000	5	1000	25	83	90
K7805-1000(L)	24(6.5-32)	5	1000	5	1000	25	88	93
K7809-1000(L)	24(12-32)	9	1000	5	1000	25	92	95
K7812-1000(L)	24(15-32)	12	1000	5	1000	25	94	96
K7815-1000(L)	24(18-32)	15	1000	5	1000	25	94	97

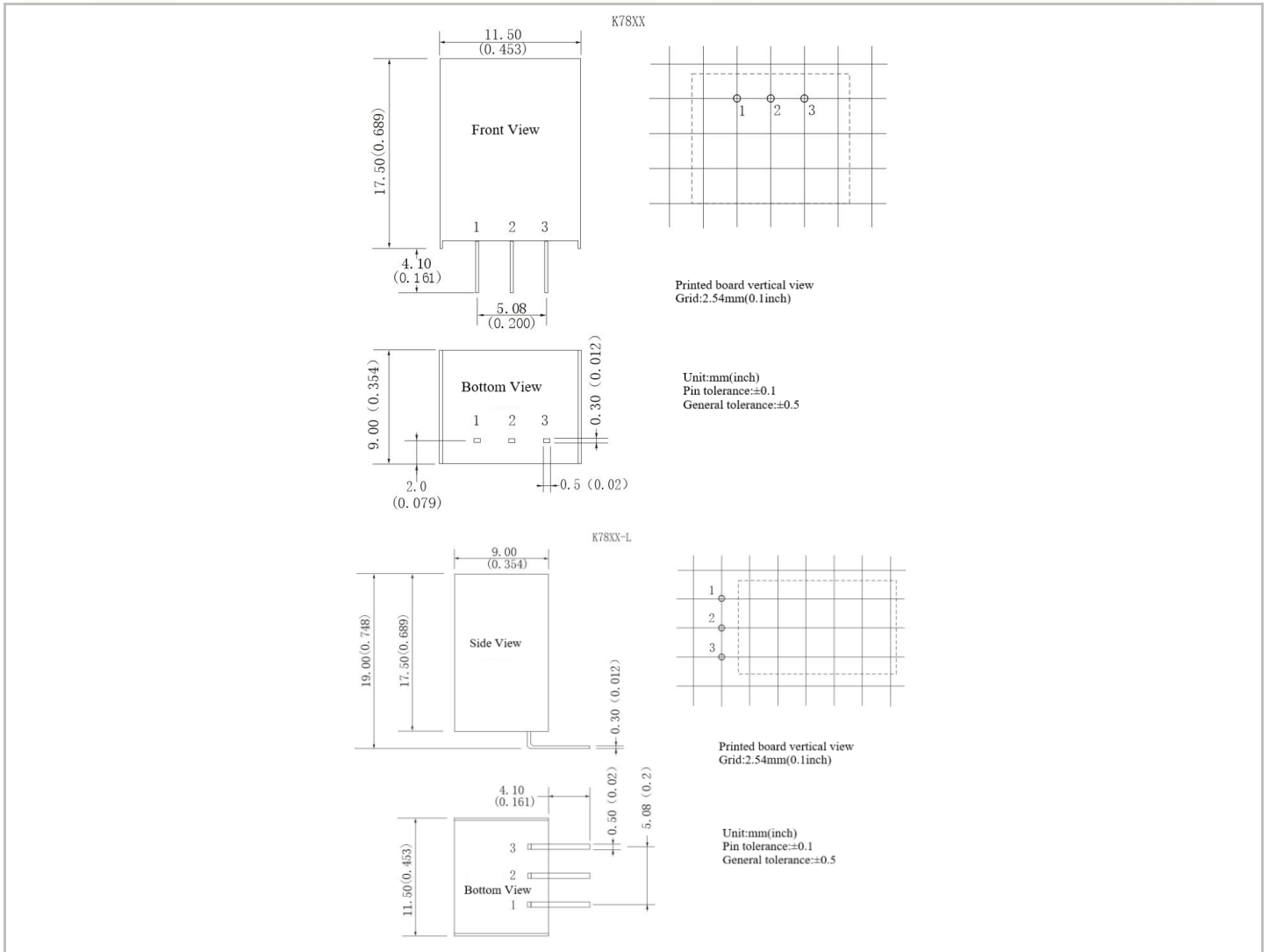
Characteristic Curve



Temperature Curve



Packing Dimension



K78XX-1000/2000 series appearance standard description

	Before Change	After Change	
1. Distance from pin to case			<p>The glue (including the glue on the pins) does not extend beyond the shell</p>
2. General Tolerance	General tolerance:±0.25	General tolerance:±0.50	
3. Appearance description of glue	No	1.some glues on it, as long as it does not exceed the shell, and the components are not exposed. 2.When there is little glue, it is also acceptable to have component marks (not exposed)	
4. Deflection of Pin	No	pin skewed with 10°	

Packing Code	L x W x H	
K78XX-1000	11.5*9.0*17.5mm	0.453 x 0.354 x 0.689inch



Pin-Function

Pin-Out	1	2	3
Single(S)	+Vin	GND	+Vout

Note: 1.K78XX-1000(L) is bend pin.
2.If the definition of pin is not in accordance with the manual, please refer to the label on actual item.

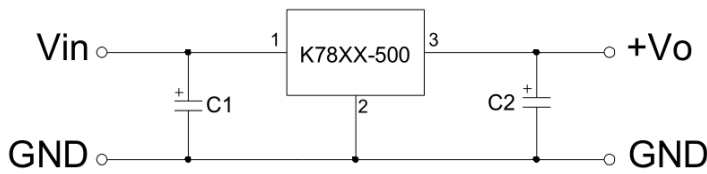
Design and Application Circuit Reference

1. Output Load Request

- a. To ensure this module operate efficiently and reliably, the minimum load could not be less than 10% of the nominal load. If the actual power is too small, please parallel a resistor at output terminal, the resistance equal to 10% of nominal load.
- b. The maximum capacitive load is tested under nominal input voltage with full load, and cannot exceed the maximum capacitive load of output side when using, or it will be difficult to start up and damage the product.

2. Recommended Circuit

In order to ensure that the ripple& noise of input and output be decreased, a capacitor filter net could be connected to input output side, application circuit see photo 1; But a proper filter capacitor should be chosen, start up problem maybe caused by too big capacitance; to ensure each circuit work safely and reliably, recommended capacitive load value see below table 1.(The capacitance of C1,C2 refer to external capacitor table, and could be increased properly, also could use low ESR of tantalum capacitor and electrolytic capacitor)

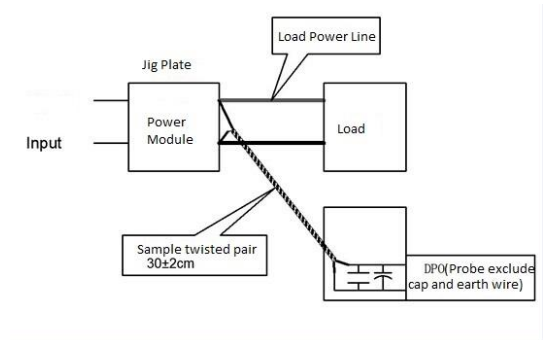


Model	C1 Ceramic capacitor	C2 Ceramic capacitor
K783V3-500	10uF/50V	22uF/6.3V
K7805-500	10uF/50V	22uF/10V
K7812-500	10uF/50V	10uF/25V
K7815-500	10uF/50V	10uF/25V

3. Ripple & Noise Test(Twisted pair method 20MHZ bandwidth)

Test Method:

- a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 47uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Note:

- 1.This product cannot be used in parallel, and do not support hot-plugging;
- 2.All index testing methods in this datasheet are based on our Company's corporate standards
- 3.The product specification may be changed at any time without prior notice.