

Typical performance

- ◆ Wide Input voltage range (2:1)
- ◆ Typical Efficiency 80%
- ◆ Switching frequency 220KHz±30KHz
- ◆ Input Overvoltage、 Under voltage Protection
- ◆ Input-output isolated
- ◆ PCB Mounting
- ◆ Sine wave output



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25°C

Input Specifications

Input voltage range	Meet Internal Current/ Voltage	Nominal 12V	9~18Vdc
	Input Voltage Range2:1	Nominal 24V	18~36Vdc
		Nominal 48V	36~72Vdc
Remote Control (Positive logic control)	ON	CNT Pin left open	
	OFF	CNT connect to -Vin	
Input under-voltage protection	Input Overvoltage、 Under voltage Protection		

Output Specifications

Output Voltage accuracy			±5Vac
Line regulation	Nominal load,full voltage rang		±1%
Load regulation	20%~100% nominal load		±1%
Output waveform		Standard Sine Wave	
Output frequency		22Hz	±3Hz
Total harmonic distortion		Nom: ±2%	Max: ±5%
Voltage Voltage adjustment		Unavailable	

General Specifications

Transfer Efficiency	Nominal Voltage Range,full load	83%(TYP.)	
Switching Frequency		250KHz (TYP.)	
Operating Temperature		Free air convection	-25°C~+85°C
Storage temperature			-55°C~+125°C

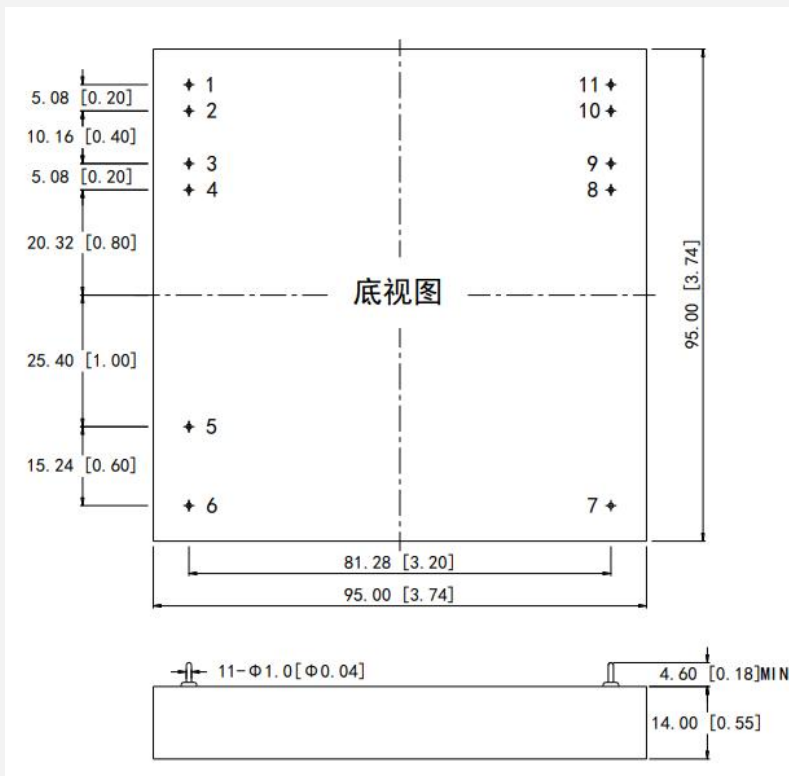
Max case temperature		+90℃
Relative Humidity		10%~90%
Case Material		Metal Case
Isolation Voltage	Input to output 500Vdc≤0.5mA/1 Min; Input to Case 500Vdc≤0.5mA/1 Min;	

Typical Products List

Module No	Input voltage	Output voltage / current				
		VO1		VO2		Frequency
		V	mA	V	mA	Hz
WR30-12S75	12V (9~18V)	75Vac	400			25
WR30-24S75	24V(18~36V)	75Vac	400			25
WR30-48S75	48V(36~72V)	75Vac	400			25
WR40-12S75	12V (9~18V)	75Vac	530			25
WR40-24S75	24V(18~36V)	75Vac	530			25
WR40-48S75	48V(36~72V)	75Vac	530			25

Note: Due to space limitations ,the above list is only for some products, If other than a list of products, please contact the Company's sales department.

Packing Dimension



Pin	1	2	3	4	5	6	7	8	9	10	11
Function	+Vin	+Vin	-Vin	-Vin	CASE	CNT	NC	Vo2	Vo2	Vo1	Vo1