

## **DC-DC Converter NW1-XXXXXAN Series**

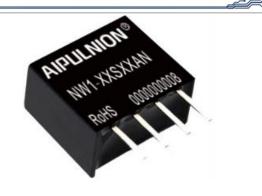






## **Typical Features**

- ◆ Fixed input voltage, Isolated & regulated output, Output power 1W
- ◆ High Efficiency up to 75%
- ◆ Small compact SIP packing
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature: -40°C~+85°C
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25°C

#### **Application Field**

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

## **Typical Product List**

Part No.	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capaciti ve Load	Ripple & Noise (Max.)	(%)@ full nomir	ciency output load, nal input	
	Nomin	Range	Voltage (VDC)	Current(mA)	Full load	No Load	uF	mVp-p	Min. Typ.	Typ	
	al	rtarigo	vollage (120)	MAX./Min.	Тур.	Тур.	ui			1 yp.	
NW1-05S3V3AN	_	4.75	3.3	250/25	290	6	2400	80	67	70	
NW1-05S05AN	5	٥ ا	- 5.25	5	200/20	265	6	2400	80	70	73
NW1-05S12AN		0.20	12	84/9	260	8	560	80	71	74	
NW1-12S3V3AN		11.4	3.3	250/25	110	8	2400	80	67	70	
NW1-12S05AN	12	- 12.6	5	200/20	108	8	2400	80	70	73	
NW1-12S12AN		12.0	12	84/9	107	8	560	80	71	74	
NW1-24S3V3AN	24	24	22.8	3.3	250/25	56	8	2400	80	67	70
NW1-24S05AN			-	5	200/20	54	8	2400	80	70	73
NW1-24S12AN		25.2	12	84/9	52	8	560	80	71	74	

In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.

## **Input Specifications**

Item	Test Condition	Min.	Тур.	Max.	Unit		
Input Overshoot Voltage	5Vdc Input	-0.7	-	9	VDC		
(1Second.max.)	12Vdc Input	-0.7	-	18	VDC		



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		24Vdc Input	-0.7		-		30	
Input Filter			Capacitor Filter					
Output S	pecifications							
	ITEM	Working Conditions	1	Min.	Тур.	Max.	Unit	
Output Power			(	0.07		1	W	
Output Vo	oltage Accuracy	Nominal input, Full load	d		±2	±3		
Load	Regulation	10% ~ 100% nominal loa	ad			±3	%	
Line Volta	age Regulation	Input Voltage Change±1	%			±0.25		
Ripple	e & Noise①	Nominal input, full load,20l bandwidth	MHZ		35	80	mVp-p	
Temperatur	e Drift Coefficient	100% Load				±0.03	%/°C	
Output Short Circuit Protection			Conti	nuous, :	self-recove	ry		
NOTE: 1 R	tipple & Noise Tested	d by twisted-pair method, for de	etails please	check D	Design and	Application (	Circuit.	
General S	Specifications							
Switc	ching Frequency	typical	typical		260KHz (Typ.)			
Operating Temperature		Refer to Temperature De	Refer to Temperature Derating Curve -40 °C ~ +85 °C		· <b>+85</b> ℃			
Storage Temperature					-55℃ ~+125℃			
Shell temperature rise during work		ork Within Temperature Der	rating Curve		25℃(Typ.)			
Relative Humidity		No condensir	ng		5%~95%		95%	
С	ase Material			Blac	Black flame-retardant heat-resistant Plastic(UL94 V-			
Pin with	stand welding temp	Distance to case 1.5	mm, 10s		300℃ MAX			
Iso	olation Voltage	Test 1 minute, leakage 0.5mA	e current<		1500Vdc			
Isola	ation Capacitor	Input/Output, 100KI	Hz/0.1V		20 pF (Typ.)			
	MTBF	MIL-HDBK-217F@	25℃		35X10⁵Hrs		)⁵Hrs	
Pr	oduct Weight			1.4g(Typ.)		Тур.)		
	Package	Tube(525*18*10	Tube(525*18*10mm)		43PCS			
Package		Inner Box(542*110*	Inner Box(542*110*155mm)		3440PCS(Total 80Tubes)			
EMC Cha	racteristics							
CE		CISPR32/EN55032 C	CISPR32/EN55032 CLASS B(see EMC recommended circuit)					
EMI	RE	CISPR32/EN55032 C	CISPR32/EN55032 CLASS B(see EMC recommended circuit)					
EMS	ESD IEC/EN610		Air±8kV, Cont	act±6k	V perf.C	riteria B		
Packing Dimension								

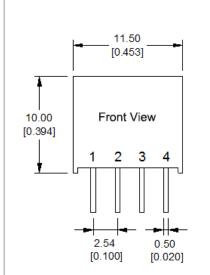


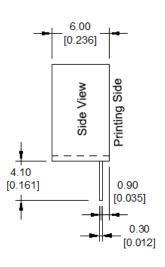
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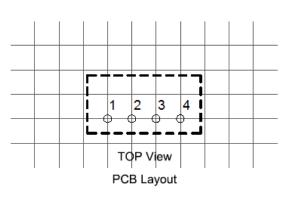












Note: Grid 2.54x2.54mm unit:mm[inch] pin tolerance:±0.10[±0.004]

general tolerance:±0.50[±0.020]

Packing Code LxWxH Α 11.50× 6.00 × 10.00mm  $0.453 \times 0.236 \times 0.394$ inch **Pin Function** 1 2 3 4 Single(S) **GND** +Vin -Vo +Vo

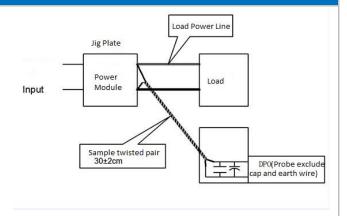
Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

#### Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

#### Test Method:

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

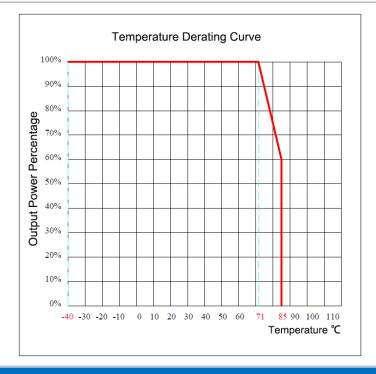
b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



## **Product Characteristic Curve**





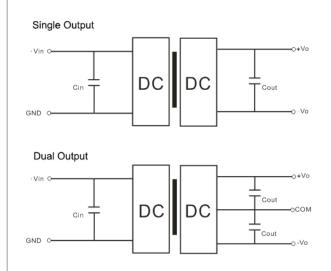


## **Design and Application Circuit Recommended**

- 1. Output load requirements
- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

### 2. Recommended circuit

a. In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1.



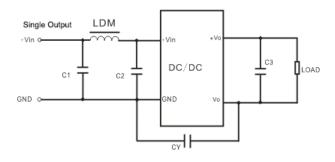
#### Recommended capacitive load value(Table 1)

Vin (Vdc)	Cin	Single Vout Vdc	Cout (μF)	Dual Vout (Vdc)	Cout (µF)
5	10 µF/16V	3. 3	10 µF/16V	±3.3	4.7μF/16V
12	2. 2 µ F/25V	5	10 µ F/16V	±5	4.7μF/16V
15	2. 2 µ F/25V	9	2. 2 µF/25V	±9	2. 2 μF/25V
24	1μF/50V	12	2.2 µF/25V	±12	1μF/25V
		15	1μF/25V	±15	1μF/16V
		24	1μF/50V	±24	0. 47 μF/50V

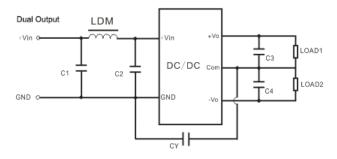




#### 3. EMC typical recommended circuit



Input Vo	ltage	5VDC	12/15/24VDC		
ЕМІ	C1/C2	<b>4. 7</b> μF/ <b>16</b> V	4. 7 μF/50V		
	CY	270pF/2kV	270pF/2kV		
	С3	. Refer to Cout Spes	according to Table 1		
	LDM	6.8µH	6.8µH		



Input \	/oltage	5VDC	12/15/24VDC	
ЕМІ	C1/C2	<b>4. 7</b> μF/ <b>16</b> V	4.7μF/50V	
	CY	270pF/3kVdc	270pF/3kVdc	
	C3/C4	Refer to Cout Spes	according to Table 1	
	LDM	6.8µH	6. 8 µ H	

#### Note:

- 1. This product cannot be used in parallel, and do not support hot-plugging;
- 2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;
- 3. All index testing methods in this datasheet are based on our Company's corporate standards
- 4. The product specification may be changed at any time without prior notice.