



Typical Feature

- ◆ Fixed Input Voltage, isolated & regulated Output, power 1W
- ◆ High efficiency up to 73%
- ◆ Small SMD package, international standard pin out
- ◆ Isolation Voltage 3000VDC
- ◆Operating Temperature: -40°C to +85°C
- ◆ Plastic case, meet to UL94 V-0 standard



Test conditions: Unless otherwise specified, all parameter tests are measured at nominal input voltage, purely resistive rated load and 25°C room temperature.

Application Filed

Widely used in instrumentation, communication, pure digital circuits, general low-frequency analog circuits, relay drive circuits, data exchange circuits, etc.

Typical Product List

	Input voltage range (VDC)		Output		Input		Max	Ripple&		ciency	
			Voltage/Current(Vo/Io)		Current(mA)		сара	Noise	(%)@1	ull load,	
Part No	Nominal Range	Range	ange Voltage (VDC)	Current (mA)MAX./	Full load	No load	uF	mVp-p	Min.	Тур.	
				Min.	typ.	typ.					
NW1-05S3V3A3NT	5	5	4.75	3.3	250/25	290	6	2400	80	67	70
NW1-05S05A3NT			-	5	200/20	265	6	2400	80	70	73
NW1-05S12A3NT			5.25	12	84/9	260	8	560	80	70	73
NW1-12S3V3A3NT		11.4	3.3	250/25	110	8	2400	80	67	70	
NW1-12S05A3NT	12	-	5	200/20	108	8	2400	80	70	73	
NW1-12S12A3NT		12.6	12	84/9	107	8	560	80	70	73	
NW1-24S3V3A3NT	24	22.8	3.3	250/25	56	8	2400	80	67	70	
NW1-24S05A3NT		24	24 -	5	200/20	54	8	2400	80	70	73
NW1-24S12A3NT		25.2	12	84/9	52	8	560	80	70	73	

In order to ensure that the module can work efficiently and reliably, when in use, the minimum output load cannot be less than 10% of the rated load. If the power you need is really small, please connect a resistor in parallel at the output end, the recommended resistance is equivalent to 10% of the rated power.

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Item	Item Operating Condition		Тур.	Max.	Unit
Input Overshoot Voltage (1sec.max.)	5Vdc Input	-0.7 9			
	12Vdc Input	-0.7		18	VDC
	24Vdc Input	-0.7		30	
Input Filter Type		Capacitor Filter			



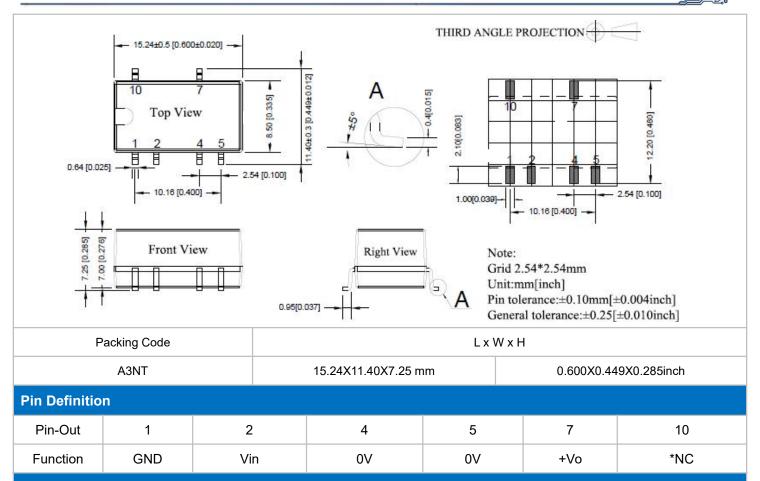


Output Sp	ecifications						
lt	tem	Operating Condition Min.		Тур.	Max.	Unit	
Outpu	ıt Power		0.1		1	W	
Output Volt	age Accuracy	Nominal input, full load -		±2	±3		
Load R	Regulation	10%-100% load	-		±3	%	
Line R	egulation	Input voltage change ±1%	Input voltage change ±1% -		±0.25		
Ripple 8	R Noise ①	Nominal input, full load, 20MHZ bandwidth	-	35	80	mVp-p	
-	ature Drift	100% load	-	-	±0.03	%/°C	
Short Circu	uit Protection	Contin	uous, Self-re	ecovery			
Note: ① rip	ple & noise is te	sted by Twisted pair method.					
General S	pecifications						
Switching Frequency		Typical		260KHz (Typ.)			
Operating Temperature		see Temperature Derating Curve	see Temperature Derating Curve		-40℃ ~ +85℃		
Storage Temperature				-55℃ ~+125℃			
Reflow Temperature		Peak temperature Tc≤250˚ℂ, m	Peak temperature Tc≤250°C, maxi time is 60S for temperature above 217°C				
Case Temperature Rise		Within temperature derating curve		25℃(Typ.)			
Relative	Humidity	non-condensing	5%~95%				
Case Material				Black flame-i	retardant, heat-re	sistant plas	
Pin solderin	g temperature	10 seconds at a distance of 1.5mm from	ı case	300°C MAX			
Isolation Voltage		Test 1min, leakage current≤0.5mA	Test 1min, leakage current≤0.5mA 3000Vc		3000Vdc		
Isolation Capacitor		Input-output, 100KHz/0.1V	Input-output, 100KHz/0.1V		20 pF (Typ.)		
MTBF		MIL-HDBK-217F@25℃			35X10 ⁵ Hrs		
Product Weight		-			1.4g (Typ.)		
EMC Char	acteristic						
CE		CISPR32/EN55032 CLASS B(see B	CISPR32/EN55032 CLASS B(see EMC recommended circuit)				
EMI RE		CISPR32/EN55032 CLASS B(see B	CISPR32/EN55032 CLASS B(see EMC recommended circuit)				
EMS	ESD	IEC/EN61000-4-2 Air±8kV, Conta	act±6kV p	erf.Criteria B			

Packing Information



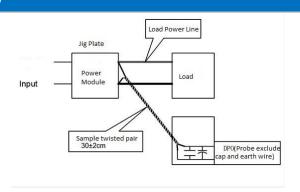




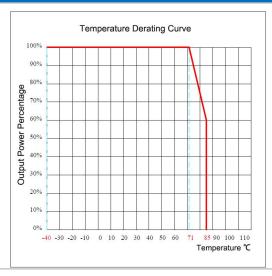
Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

Test Method:

- a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Products Characteristic Curve





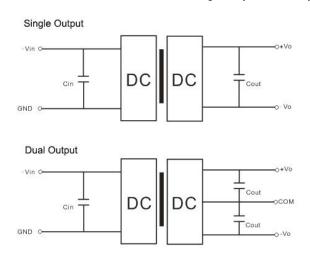


Application Circuit

- 1. Output load requirements
- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

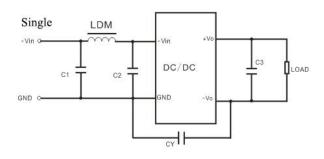
In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1.



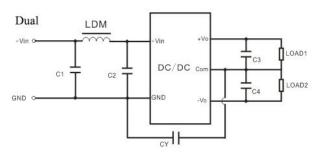
Recommended capacitive load value(Table 1)

Vin (Vdc)	Cin	Single Vout Vdc	Cout (µF)	Dual Vout (Vdc)	Cout (μF)
5	10 µF/16V	3. 3	10 µF/16V	±3.3	4.7 µ F/16V
12	2. 2 µ F/25V	5	10 µ F/16V	±5	4. 7 μ F/16V
15	2. 2 µ F/25V	9	2, 2 µ F/25V	±9	2.2 µF/25V
24	1 µ F/50V	12	2. 2 µF/25V	±12	1μF/25V
		15	1μF/25V	±15	1μF/16V
		24	1μF/50V	±24	0. 47 µ F/50\

3. EMC recommended circuit



Input Voltage		5VDC	12/15/24VDC	
	C1/C2	4.7µF/16V	4. 7 μ F/50V	
	CY	270pF/3kV	270pF/3kV	
EMI	СЗ	Refer to Cout	pecs at Table 1	
	LDM	6.8 µ H	6.8 µ H	

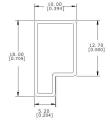


Input Voltage		5VDC	12/15/24VDC	
	C1/C2	4.7µF/16V	4.7µF/50V	
	CY	270pF/3kVdc	270pF/3kVdc	
EMI	C3/C4	Refer to Cout s	pecs at Table 1	
	LDM	6.8 µ H	6.8 µ H	



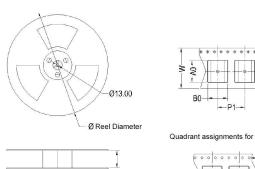


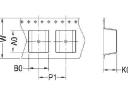
Packing Information



Unit:mm(inch) General tolerance: ±1.50[±0.059] Single tube packing qty:33pcs Carton packing qty:2640pcs Size of single tube:525x18x10mm Size of carton:542x110x155mm







Quadrant assignments for Pin 1 orientation in tape

Sprocket holes

Q1 Q2 × User Direction of feed Pocket Quadrants

Packing method: Tube

Packing method: Tape and reel(500pc per reel)

Note:

- 1. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
- 2. The maximum capacitive load is tested under nominal input voltage range and full load condition;
- 3. Unless otherwise specified, data in this datasheet are tested under conditions of Ta=25℃, humidity<75% when inputting nominal voltage and outputting rated load(pure resistance load);
- 4. All index testing methods in this datasheet are based on our Company's corporate standards.
- 5. We can provide customized product service;