

Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, Output power 2W
- ◆ High transfer efficiency up to 84%
- ◆ Continuous short circuit protection
- ◆ Full load 8kV impact withstand voltage
- ◆ Isolation voltage 5000VAC or 7000VDC, Reinforced isolation
- ◆ Operating Temperature: -40℃~+105℃
- ◆ Isolation capacitance as low as 7pF
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25℃

Application Field

NN2-XXSXXD7R3 is specially designed for applications in high-voltage power systems such as photovoltaics and energy storage that require the generation of a set of voltages isolated from the input power supply. This product is suitable for:

1. The voltage of the input power supply is relatively stable (voltage change range $\pm 10\%V_{in}$);
2. The isolation voltage required between input and output is $\leq 5000VAC$ or $7000VDC$.

Typical Product List

Part No.	Input Voltage Range (VDC)		Output Voltage/ Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitive Load	Ripple & Noise (Max.)	Efficiency (%)full load, input nominal voltage	
	Nominal	Range	Voltage (VDC)	Current(mA)	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Typ.
*NN2-12S05D7R3	12	10.8	5	400/40	210	20	1000	150	80	83
NN2-12S12D7R3		- 13.2	12	167/17	200	20	470	150	81	84

1. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.
2. "*" is model under developing.

Input Specifications

Item	Working Conditions	Min.	Typ.	Max.	Unit
Input Overshoot Voltage (1Second.max.)	5Vdc Input	-0.7	--	18	VDC
Input Filter	Capacitor Filter				
Hot Plug	N/A				



Output Specifications

Item	Working Conditions		Min.	Typ.	Max.	Unit
Output Power			0.2	--	2	W
Output Voltage Accuracy	Nominal input, Full load		--	±3	±5	%
Load Regulation	10% ~ 100% nominal load	5Vdc output	--	--	±15	
		12Vdc output	--	--	±20	
Line Voltage Regulation	Input Voltage Change±1%		--	--	±1.2	
Ripple & Noise①	Nominal input, full load, 20MHZ bandwidth		--	50	150	mVp-p
Temperature Drift Coefficient	100% Full Load		--	--	±0.03	%/°C
Output Short Circuit Protection	Continuous short-circuit protection, self-recovery					

NOTE:①Ripple & Noise tested by twisted-pair method;

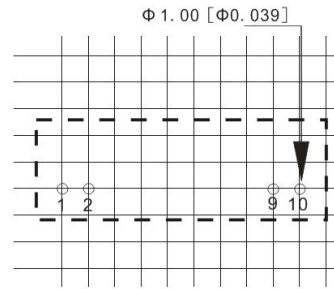
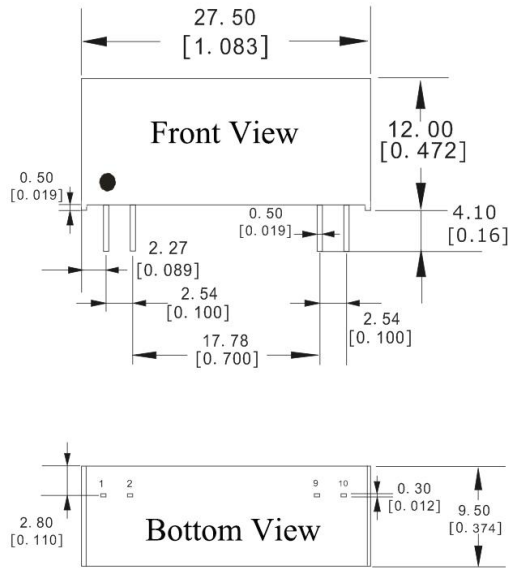
General Specifications

Switching Frequency	Typical	260KHz (Typ.)
Operating Temperature	Temperature ≥71°C, use with derating, refer to the temperature derating curve.	-40°C ~ +105°C
Storage Temperature		-55°C ~ +125°C
Wave soldering temperature	Peak temperature Tc≤245°C, maximum time above 217°C is 60S	
Shell temperature rise during work	Within Temperature Derating Curve	25°C(Typ.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic(UL94 V-0)
Pin withstand solder temperature	Distance to case 1.5mm, 10Seconds	300°C MAX
Isolation Voltage	Input to Output, test 1 minute, leakage current<0.5mA	≤5000VAC
		≤7000VDC
Insulation Resistance	Input-output, isolation voltage 500VDC	1000MΩ
Isolation Capacitor	Input/Output,100KHz/0.1V	7 pF (Typ.)
MTBF	MIL-HDBK-217F@25°C	35X10 ⁵ Hrs
Product Weight		5.3g (Typ.)

EMC Characteristics

EMI	conducted disturbance	CISPR32/EN55032 CLASS B(See EMC recommended circuit diagram)
	Radiation harassment	CISPR32/EN55032 CLASS B(See EMC recommended circuit diagram)
EMS	electrostatic discharge	IEC/EN61000-4-2 Air±8kV,Contact±6kV perf.Criteria B

Packing Dimension



Note:grid 2.54*2.54mm
 unit:mm(inch)
 pin tolerance: $\pm 0.10[\pm 0.004]$
 general tolerance: $\pm 0.50[\pm 0.020]$

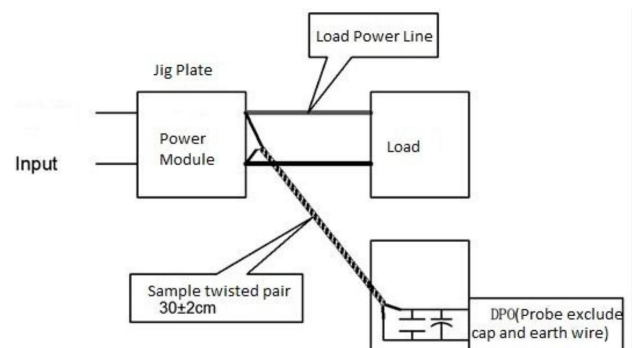
Packing Code	L x W x H	
D7	27.50X9.50X12.00 mm	1.083X0.374X0.472inch

Pin Function

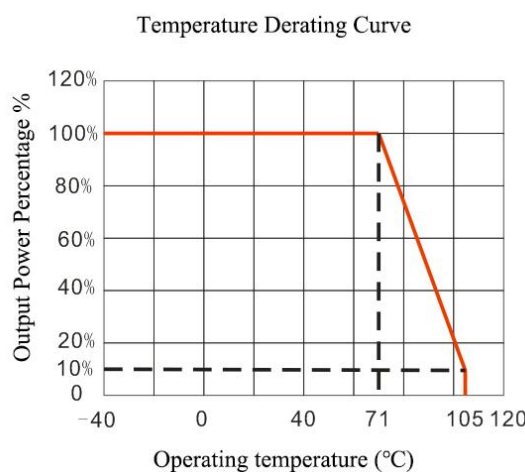
Pin Function	1	2	9	10
Single(S)	Vin	GND	0V	+Vo

Ripple & Noise Test: (Twisted Pair Method 20MHZ bandwidth)

a. 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
 b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Temperature Curve



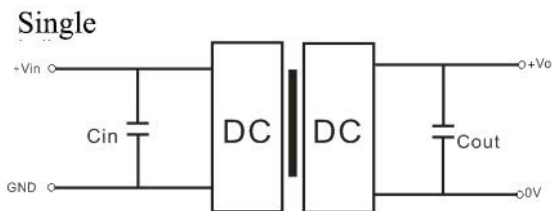
Design and Application Circuit Recommended

1. Output load requirements

- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

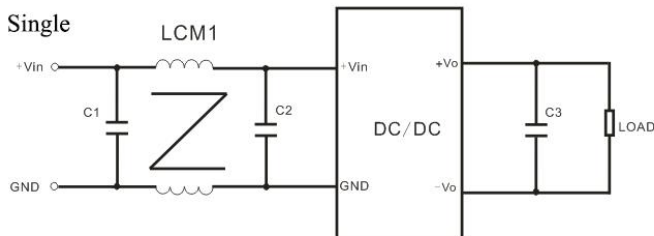
In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1.



Recommended capacitive load value(Table 1)

Vin	Cin	Vout	Cout
12Vdc	4.7 μF/25V	5Vdc	10 μF/16V
		12Vdc	4.7 μF/25V

3. EMC typical recommended circuit



Input Voltage		12VDC
EMI	C1/C2	22 μF / 25V
	C3	Refer to the Cout specs in Table 1
	LCM1	4.7mH

- Note:
- 1. This product cannot be used in parallel, and do not support hot-plugging;
 - 2. If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;
 - 3. The maximum capacitive load is tested under input voltage range and full load conditions;
 - 4. Unless otherwise specified, all indicators in this manual are measured at Ta=25°C, humidity <75%RH, nominal input voltage and output rated load;
 - 5. All index testing methods in this datasheet are based on our Company's corporate standards
 - 6. The product specification may be changed at any time without prior notice.