



Product Typical Features

- ◆ Wide input voltage range (4:1), Output Power 12W
- ◆ Transfer Efficiency up to 88%
- ◆ Stand-by Power Consumption as low as 0.020W
- ◆ Output super-fast start up
- ◆ Continuous Short Circuit protection, Self-recovery
- ◆ Input under voltage, output over voltage, short circuit, over current protection
- ◆ Switching Frequency 250KHz
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature: -40°C~+85°C
- ◆ Good EMI performance
- ◆ International standard pin-out



Application Field

FD12-XXDXXA3(C) is a newly designed DIP 1X1 packed, 12W output power, ultra wide input range 4:1, low stand-by power consumption, isolated regulated output DC-DC converter, could be widely used for industrial control, instrument, communication, power electricity, internet of things field. For harsh EMC environment, the application circuit in the datasheet is strongly recommended.

Typical Product List

Part No	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current (mA) Nominal Voltage		Max. Capacitive Load uF	Ripple & Noise		Efficiency (%)@output full load, input nominal voltage	
	Nominal	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load typ.	No Load typ.		mVp-p		Min	Typ
								Typ.	Max.		
FD12-18D3V3A3(C)	24	9-36	±3.3	±1200/0	413	1	5000	120	140	77	80
FD12-18D05A3(C)	24	9-36	±5	±1200/0	600	3	3000	120	140	80	83
FD12-18D09A3(C)	24	9-36	±9	±667/0	588	4	2000	120	140	82	85
FD12-18D12A3(C)	24	9-36	±12	±500/0	568	5	1500	120	140	85	88
FD12-18D15A3(C)	24	9-36	±15	±400/0	581	5	1000	120	140	83	86
FD12-18D24A3(C)	24	9-36	±24	±250/0	568	5	300	140	180	85	88
FD12-36D3V3A3(C)	48	18-75	±3.3	±1200/0	207	1	5000	120	140	77	80
FD12-36D05A3(C)	48	18-75	±5	±1200/0	301	1	3000	120	140	80	83
FD12-36D09A3(C)	48	18-75	±9	±667/0	294	1	2000	120	140	82	85
FD12-36D12A3(C)	48	18-75	±12	±500/0	287	4	1500	120	140	84	87



FD12-36D15A3(C)	48	18-75	±15	±400/0	287	1	1000	120	140	84	87
FD12-36D24A3(C)	48	18-75	±24	±250/0	284	1	500	120	140	85	88

1. “*” are models being developing;
2. “-T” suffix for chassis mounting, “-TS” suffix for DIN-Rail mounting, DIN-Rail width is: 35mm;
3. Max capacitive load is, when the power supply is fully loaded, the max capacity could be connected to output, if exceed, the power supply cannot start-up;
4. To reduce no load power consumption and improve efficiency of light-load, IC will be flitter frequency under no-load and light-load operating, output cannot be no load, at least with 10% load or above 470uF high frequency low resistance electrolytic capacitor, otherwise the output ripple will rise;

Input Specification

Stand-by Consumption	0.05 W(TYP)		
Input Filter	π filter		
Input Under-Voltage Protection	5~9VDC@FD12-18DXXA3(C) Input		
	11~18VDC@FD12-36DXXA3(C) Input		
CTRL*	Module turn-on	CTRL suspended or TTL high level (2.5-12VDC)	
	Module turn-off	CTRL connect to GND or low level (0-1.2VDC)	
	Input current when switched off	5mA (TYP)	

Note: *The voltage of CTRL pin is relative to GND pin.

Output Specification

Main Circuit Output Voltage Accuracy	Full voltage full load	Vo	±2.0%(max)
Auxiliary Circuit Output Voltage Accuracy	Full voltage full load	Vo	±3.0%(max)
Voltage Regulation	Nominal load, full voltage	Vo	Main circuit: ≤±0.5%; auxiliary circuit ≤±1.0%
Load Regulation	10% ~ 100% nominal load	Vo	≤±1.0%
Ripple & Noise	Nominal load, nominal voltage Twisted Pair Method, 20M Hz bandwidth;	≤15% load	5%Vo mVp-p typ
		≥15% load	120mVp-p typ, 140mVp-p max
Output Over-voltage Protection	120%~200%Vo		
Output Over-load Protection	110%~220%Io		
Output Short circuit Protection	Continuous, Self-recovery		
Dynamic Response	25% nominal load step change ΔVo/Δt	3.3V、5V output	±5% typ , ±8% max /500us
		Other Output	±3% typ , ±5% max /500us



Cross Regulation	Main Circuit 50% load, Auxiliary Circuit 10~100% load	≤±5.0%
Turn-on delay time	Typical	250ms
Output Turn-on Overshoot Voltage	≤10%Vo	

Note: There may be short circuit and no self-recovery phenomenon at high voltage of several models, and the circuit could be normal after restarting.

General Specification

Switching Frequency	Typical	250KHz
Operating Temperature	Refer to Temperature Derating Curve	-40°C ~ +85°C
Storage Temperature		-55°C ~ +125°C
Max Case Temperature	Within Operating Curve	+105°C
Relative Humidity	No condensing	5%~95%
Case Material		Aluminum Metal Case
Cooling Method		Free air convection
Isolation Voltage	Input to Output	1500Vdc ≤ 0.5mA / 1min
Meantime Between Failure	MIL-HDBK-217F@25°C	2X10 ⁵ Hrs
Product Weight	Average	18g

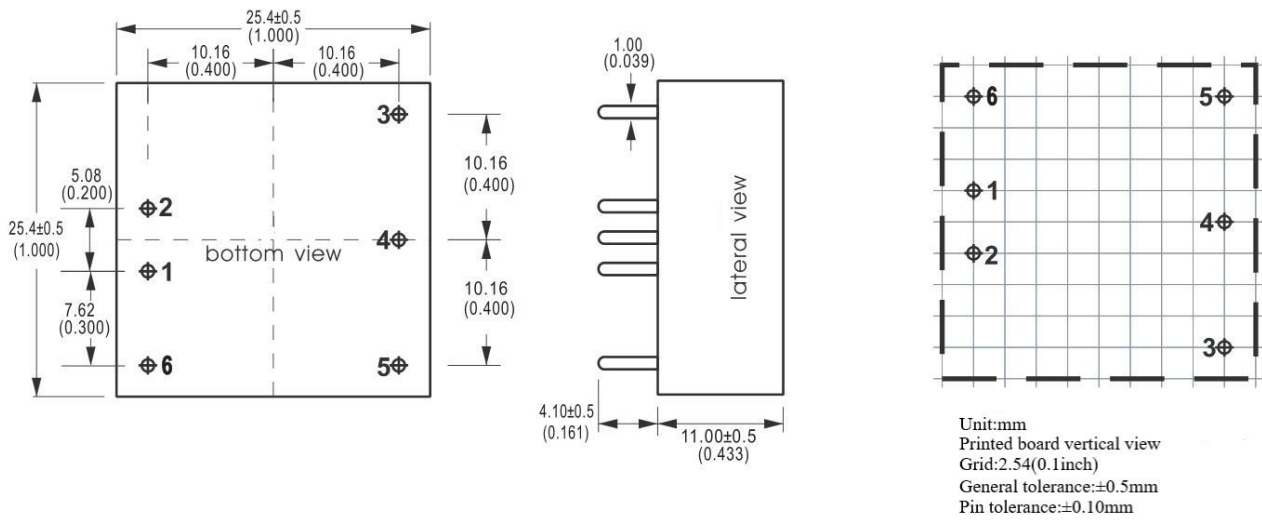
EMC Characteristics

Total Items		Sub Items	Test Standard	Class
EMC	EMI	CE	CISPR22/EN55032	CLASS B (see recommended circuit photo ②)
		RE	CISPR22/EN55032	CLASS B (see recommended circuit photo ②)
	EMS	RS	IEC/EN61000-4-3	10V/m Perf.Criteria B (see recommended circuit photo 2)
		CS	IEC/EN61000-4-6	3Vr.m.s Perf.Criteria B(see recommended circuit photo 2)
		ESD	IEC/EN61000-4-2	Contact ±4KV Perf.Criteria B
		Surge	IEC/EN61000-4-5	±2KV Perf.Criteria B (see recommended circuit photo 1)
		EFT	IEC/EN61000-4-4	±2KV Perf.Criteria B (see recommended circuit photo 1)
		Voltage dips and interruptions	IEC/EN61000-4-11	0%~70% Perf.Criteria B

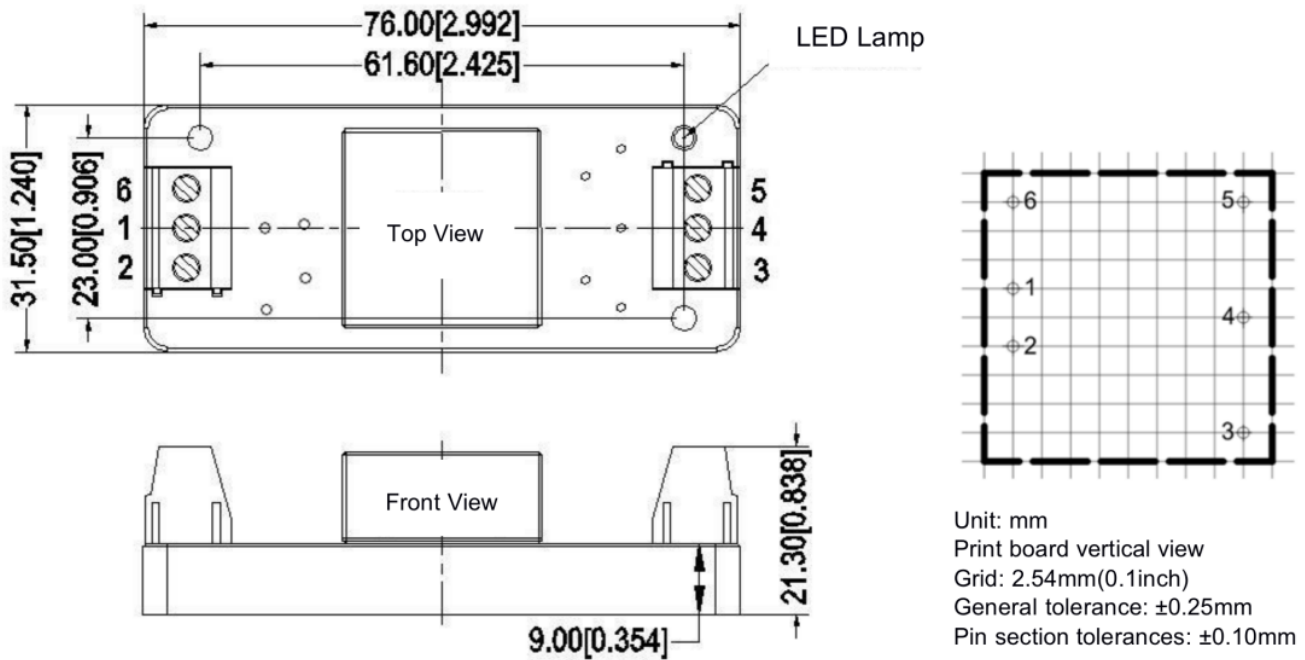
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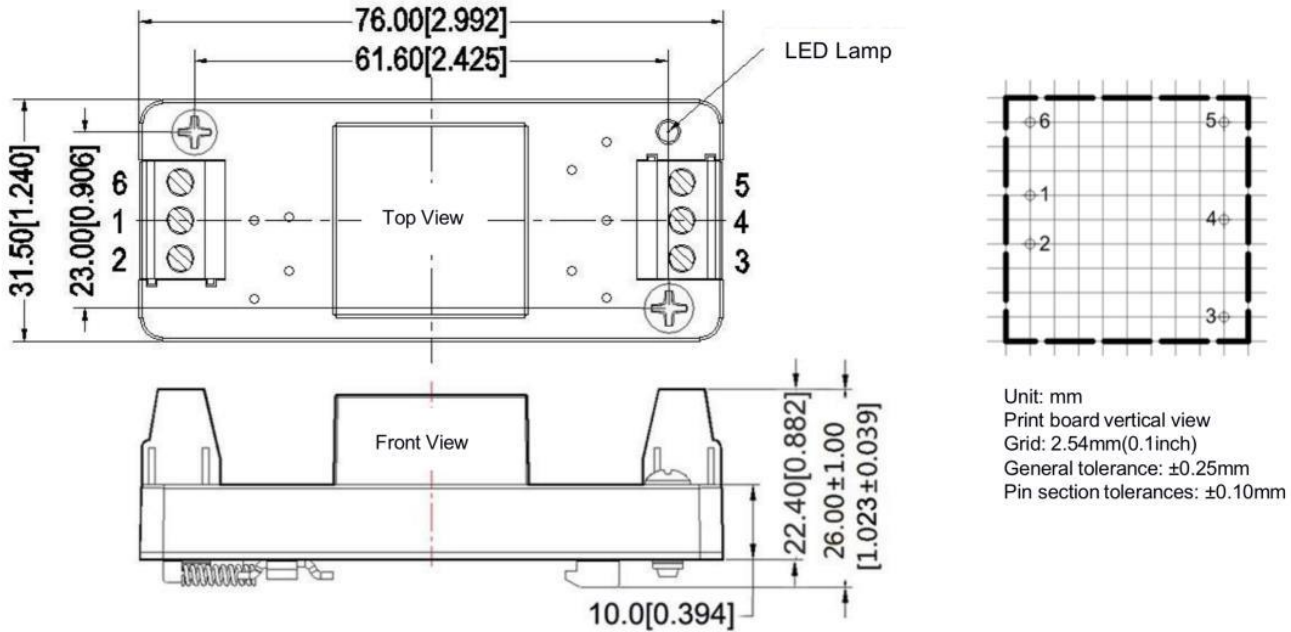
A3 Packing Dimension



A3-T Packing Dimension



A3-TS Packing Dimension



Unit: mm
 Print board vertical view
 Grid: 2.54mm(0.1inch)
 General tolerance: ±0.25mm
 Pin section tolerances: ±0.10mm

Packing Code	L x W x H	
A3	25.4X 25.4X11 mm	1X1 X0.433inch
A3-T	76X31.5X21.3mm	2.99X1.24X0.838inch
A3-TS	76X31.5X26mm	2.99X1.24X1.023inch

Pin Definition

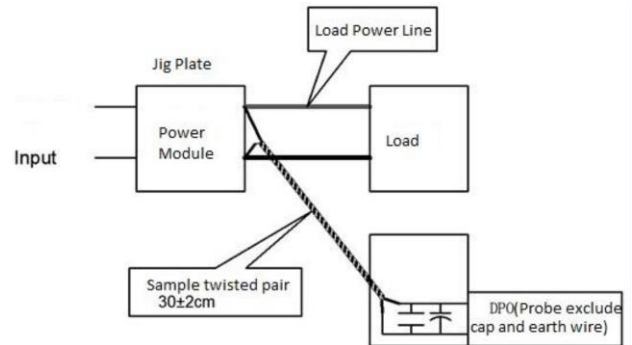
Series	1	2	3	4	5	6
FD12-XXDXXA3	-Vin	+Vin	+Vout	COM	-Vout	NP
FD12-XXDXXA3C	-Vin	+Vin	+Vout	COM	-Vout	CTRL

Note: If the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Ripple & Noise Test: (Twisted Pair Method 20MHZ bandwidth)

Test Method:

- 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm ± 2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

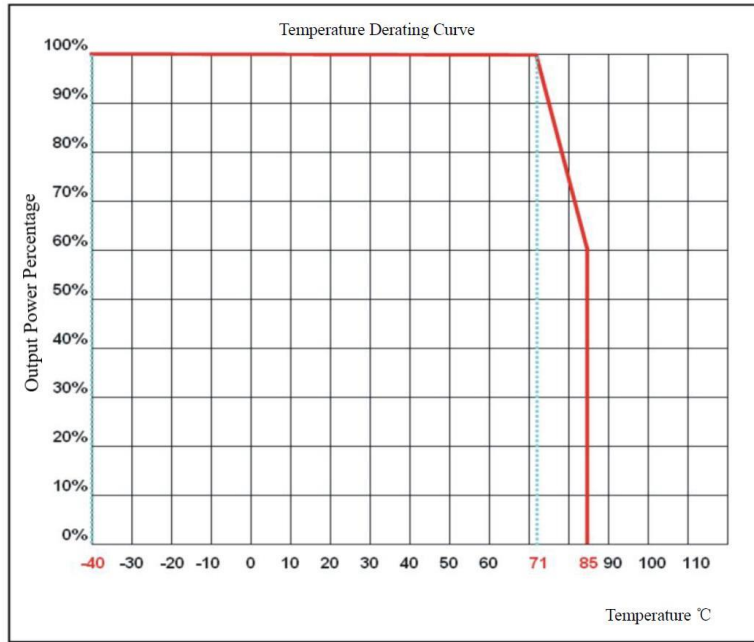


Application Reference:

- 1.Recommended minimum load is 10% or above 470uF high frequency low resistance electrolytic capacitor, or output ripple will rise;
- 2.Recommend the unbalance loads of dual output to be ≤±5%;



Product Characteristic Curve

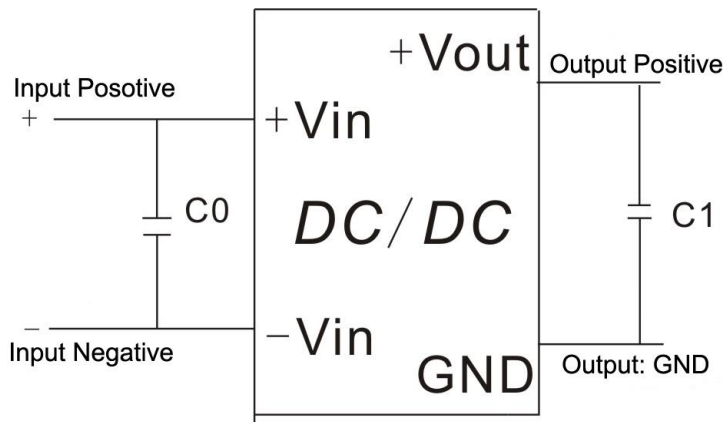


Design Application

Recommended circuit

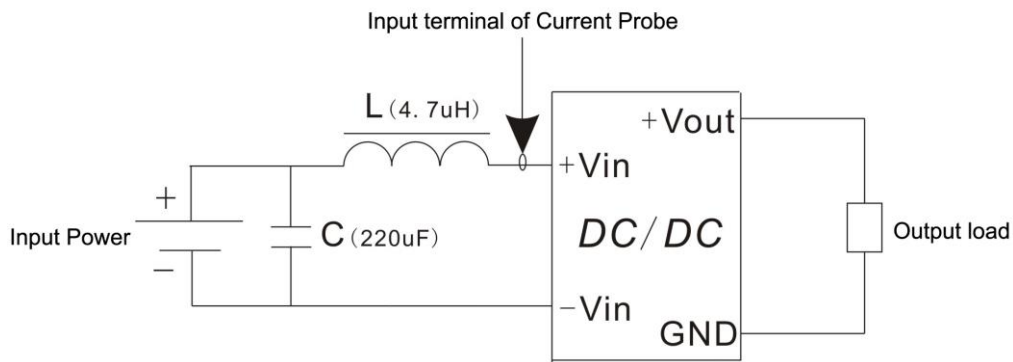
1. DC/DC test circuit:

Normal recommended capacitors: C0:47-100uF; C1:470uF.

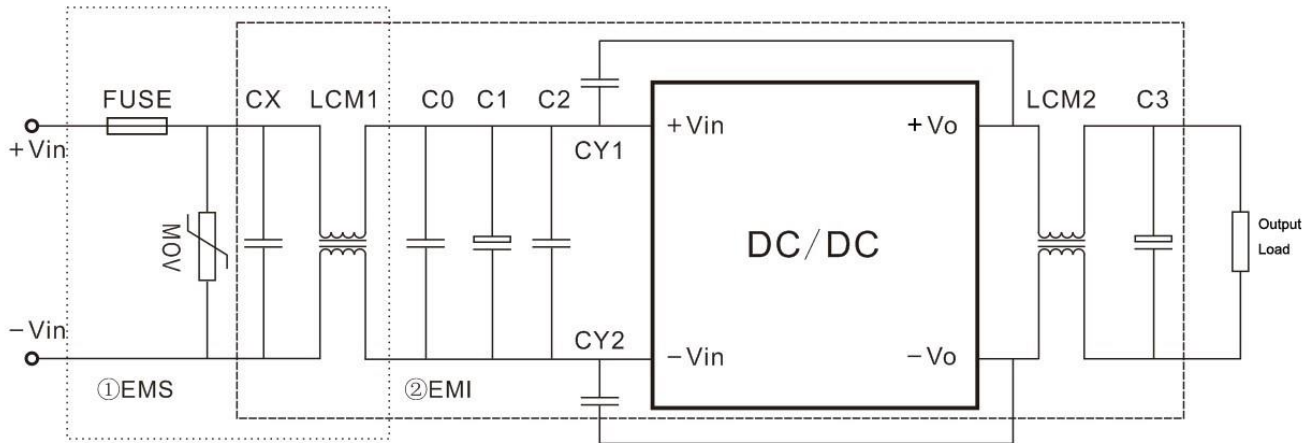


2. Input reflecting ripple current test circuit:

Capacitor C choose low ESR ones, withstand voltage value should be bigger than max input voltage;



3.EMC external recommended circuit:



Component	FD12-18DXXA3C Input	FD12-36DXXA3C Input
FUSE	According to customer's request	
MOV	14D560K	14D101K
CX	0.47uF	0.47uF
LCM1	10mH	10mH
C0	1uF/100V	1uF/100V
C1	220uF/100V	220uF/100V
C2	1uF/100V	1uF/100V
LCM2	30uH	30uH
C3	47uF/50V	47uF/50V
CY1,CY2	1nF/2000V	

Note:

1. The product should be used under the specification range, otherwise it will cause permanent damage to it.
2. If the product worked beyond the load range or below the minimum load, we cannot ensure that the performance of product is in accordance with all the indexes in this manual;
3. Unless otherwise specified, data in this datasheet should be tested under conditions of Ta=25 °C, humidity<75% when inputting nominal voltage and outputting rated load(pure resistance load);
4. All index testing methods in this datasheet are based on our Company' s corporate standards
- 5.The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technician for specific information;
6. We can provide customized product service;
7. The product specification may be changed at any time without prior notice.