

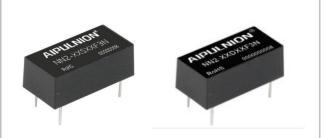






Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, Output power 2W
- ◆ High Efficiency up to 86%
- ◆ Small compact DIP packing
- ◆ Isolation Voltage 3000VDC
- ◆ Operating Temperature: -40°C ~+85°C
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25 °C

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

pical Product	List									
Part No.	Input Voltage Range (VDC)		Output Voltage/ Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitiv e Load	Ripple & Noise (Max.)	load,)full
	Nominal	Range	Voltage (VDC)	Current(mA)	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Ту
NN2-05S05F3N		4.5	5	400	476	8	2400	150	77	80
NN2-05S12F3N	5	-	12	167	455	18	1200	150	77	8
NN2-05S15F3N		5.5	15	133	455	20	560	150	77	8
NN2-12S05F3N		10.8	5	400	195	10	2400	150	81	8
NN2-12S12F3N	12	-	12	167	186	10	1200	150	83	8
NN2-12S15F3N		13.2	15	133	192	10	560	150	80	8
NN2-24S05F3N		21.6	5	400	98	8	2400	150	79	8
NN2-24S12F3N	24	-	12	167	95	8	1200	150	83	8
NN2-24S15F3N		26.4	15	133	96	8	560	150	80	8
NN2-05D05F3N		4.5	±5	±200	476	8	1200	150	77	8
NN2-05D12F3N	5	-	±12	±83	455	18	220	150	77	8
NN2-05D15F3N	1	5.5	±15	±67	455	20	220	150	77	8
NN2-12D05F3N		10.8	±5	±200	195	10	1200	150	81	8
NN2-12D12F3N	12	-	±12	±83	186	10	220	150	83	8
NN2-12D15F3N		13.2	±15	±67	192	10	220	150	80	8
NN2-24D05F3N	24	21.6	±5	±200	98	8	1200	150	79	8







NN2-24D12F3N		-	±12	±83	95	8	220	150	83	86
NN2-24D15F3N		26.4	±15	±67	96	8	220	150	80	83

Note: 1.In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.

2.the capacitive load of positive and negative output are same.

Input Specifications						
Item	Working Conditions	Min.	Тур.	Max.	Unit	
	5Vdc Input	-0.7		9		
Input Overshoot Voltage (1Second.max.)	12Vdc Input	-0.7		18	VDC	
(Tocoma.max.)	24Vdc Input	-0.7		30		
Input Filter	Capacitor Filter					

Output Specifications					
Item	Working Conditions	Min.	Тур.	Max.	Unit
Output Power		0.2		2	W
Output Voltage Accuracy	Nominal input, Full load		±2	±5	
Load Regulation	10% ~ 100% nominal load			15	%
Line Voltage Regulation	Input Voltage Change±1%			±1.2	
Ripple & Noise①	Nominal input, full load, 20MHZ bandwidth		100	150	mVp-p
Temperature Drift Coefficient	100% Full Load			±0.03	%/°C
Output Short Circuit Protection	Continuous	short-ci	rcuit protection	on, self-recover	у

NOTE: 1) Ripple & Noise tested by twisted-pair method;

General Specifications		
Switching Fraguency	5Vdc Input	260KHz (Typ.)
Switching Frequency	12Vdc/24Vdc Input	470KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40℃ ~+85℃
Storage Temperature		-55°C ~+125°C
Shell temperature rise during work	Within Temperature Derating Curve	25℃ (Typ.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic(UL94 V-0)
Pin withstand solder temperature	Distance to case 1.5mm, 10Seconds	300℃ MAX
Isolation Voltage	Test 1 minute, leakage current< 0.5mA	3000Vdc
Isolation Capacitor	Input/Output,100KHz/0.1V	20 pF (Typ.)

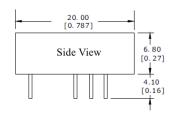


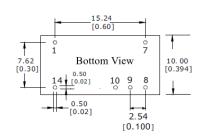


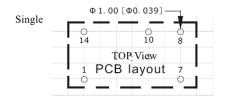


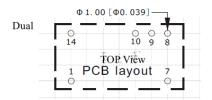
MTBF	MIL-HDBK-217F@25℃	35X10⁵Hrs	
Product Weight		2.5g (Typ.)	
Packing	Tube(525*18*10mm)	25PCS	
	Box(542*110*155mm)	2000PCS(Total 80tubes)	

Packing Dimension









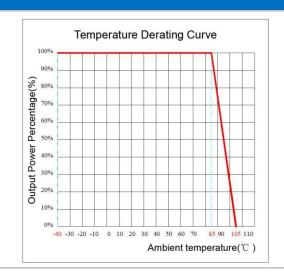
Note: Grid 2.54x2.54mm Unit:mm[inch] Pin tolerance:±0.10[0.004] General tolerance:±0.50[0.020]

Packing Code		LxWxH						
F	20.	00× 10.00 × 6.80n	nm	0.787 × 0.394 × 0.270inch				
Pin Function								
Pin Function	1	7	8	9	10	14		

Pin Function	1	/	8	9	10	14
Single(S)	GND	NC	+Vo	NP	-Vo	+Vin
Dual(D)	GND	NC	+Vo	СОМ	-Vo	+Vin

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Temperature Curve











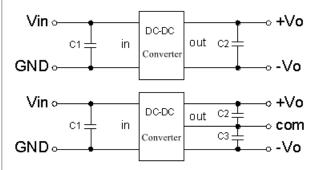
Design and Application Circuit Recommended

1. Output load requirements

- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

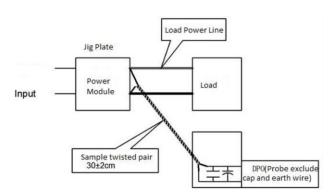
In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1.



Reco	Recommended capacitive load value(Table 1)									
Vin (Vdc)	C1 (#)	Vout (Vdc)	02 (#)	Vout (Vdc)	C2,C3 (#)					
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7					
12	2.2	9	4.7	±9	2.2					
15	1	12	22	±12	1					
24	1	15	1	±15	0.47					
		24	0.47	±24	0.22					

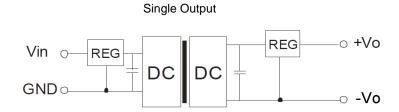
3. Ripple & Noise Test: (Twisted Pair Method 20MHZ bandwidth)

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern. b.Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



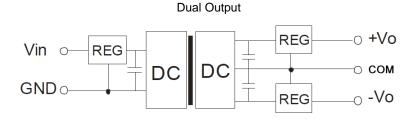
3. Output regulated voltage and over voltage protection circuit

The simplest device to protect output regulated voltage, over voltage and over current is to cascade a linear regulator with overheat protection at input or output terminal, and connect a capacitor filter net(see below picture), filter capacitive value recommended see table 1, Linear regulator is chosen according to the actual voltage, current needed in working, or choose our NW series products.









Note:

- 1. This product cannot be used in parallel, and do not support hot-plugging;
- 2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;
- 3. All index testing methods in this datasheet are based on our Company's corporate standards
- 4. The product specification may be changed at any time without prior notice.