

DC/DC Converter NN2-XXSXXA3NT Series



Typical Feature

- ◆ Fixed Input Voltage, isolated & Unregulated Single Output power 2W
- ◆ Continuous short circuit protection
- ◆ Operating Temperature: -40°C to +105°C
- Small SMD package, International standard pin-out
- ◆ Isolation Voltage 3000VDC
- ◆ High efficiency up to 86%
- ◆ No load input current as low as 5mA
- ◆ ESD meet Contact 8KV



Application Filed

NN2-XXSXXA3NT is suitable for pure digital systems, low frequency analog circuits, relay-driven circuits. It is specially designed for applications where an isolated voltage is required in a distributed power supply system. It could be widely used in the below products:

- 1. The voltage of the input power supply is relatively stable (voltage change range:±10%Vin)
- 2. Isolation between input and output is required (Isolation Voltage≤3000VDC);
- 3. Low requirements for output voltage stability and output ripple noise;

Typical Product List						
Part No	Input Voltage	Output Voltage/Current		Max.	Ripple & Noise	Efficiency
	(VDC)	Voltage	Current	Capacitive Load(Max)	20MHz (Typ/Max)	(Min/Typ)
	Range	(VDC)	(mA) Max / Min	u F	mVp-p	%
NN2-3V3S05A3NT	3.3 (2.97-3.63)	5	400/40	2400	50/100	79/82
NN2-05S3V3A3NT	5 (4.5-5.5)	3.3	600/60	2400	50/100	77/80
NN2-05S05A3NT		5	400/40	2400	50/100	80/83
NN2-05S09A3NT		9	222/22	2400	50/100	82/85
NN2-05S12A3NT		12	167/17	2400	80/100	83/86
NN2-12S05A3NT	12(10.8-13.2)	5	400/40	2400	80/100	81/84
NN2-12S12A3NT		12	167/17	2400	80/100	83/86
NN2-12S15A3NT		15	133/13	2400	80/100	81/83
NN2-24S05A3NT	24 (21.6-26.4)	5	400/40	2400	80/100	81/84
NN2-24S12A3NT		12	167/17	2400	80/100	83/86

Note 1: The typical output efficiency is based on that product is full loaded and burned-in after half an hour.

Note 2: The fluctuation range of full load efficiency(%,TYP) is ±2%, full load output efficiency= total output power/module's input power.

Note 3: Ripple & Noise Tested by twisted-pair method, for details please check Ripple &Noise Test Method.



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Item	Operating Condition		Min.	Тур.	Max.	Unit
Input Current (Full load/No load)	3.3Vdc Input	3.3Vdc output		758/10	777/15	
		5Vdc/ 9Vdc output		739/20	758/25	
		12Vdc output		722/30	739/35	
		24Vdc output	-	758/40	777/50	
		3.3Vdc output	-	500/5	513/12	
		5Vdc output	-	476/5	488/12	
	5Vdc output	9Vdc output	-	465/10	476/20	
		12Vdc output	-	455/20	465/30	
		24Vdc output	-	488/30	500/40	mA
		5Vdc output	-	200/8	235/15	-
	12Vdc Input	12Vdc output	-	190/8	235/15	
		15Vdc output		192/12	235/18	
	24Vdc input	5Vdc output	-	100/8	120/15	
		12Vdc output	-	98/8	120/15	
Reflected Ripple Current	-		-	15	-	
	3.3Vdc Input		-0.7	-	9	VDC
	5Vdc Input		-0.7	-	11	
Overshoot Voltage	12Vdc Input		-0.7	-	18	
	24Vdc Input		-0.7	-	30	
Overshoot Current	-		-	0.8	-	А
Input Filter Type	-		Capacitor Filter			
Hot Plug			Unavailable			
utput Specification	ıs					
ltem	Operatin	g Condition	Min.	Тур.	Max.	Unit
Output Voltage Accuracy	-		See Regulation Curve			
Lino Desculation	Input voltage change ±1%	3.3Vdc/5Vdc output	-	-	±1.5	%
Line Regulation		Other voltage output	-	-	±1.2	
Load Regulation	10%-100% load	3.3Vdc/5Vdc output	-	15	20	%
		Other voltage output	-	10	15	
Temperature Drift Coefficient	Full load		-	-	±0.03	%/ °C
Short Circuit Protection	_		Continuous, Self-recovery			

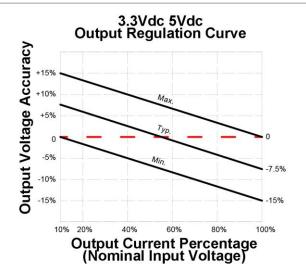


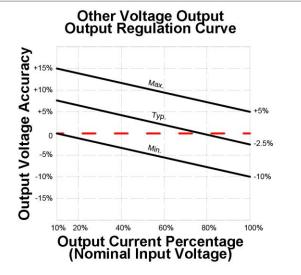
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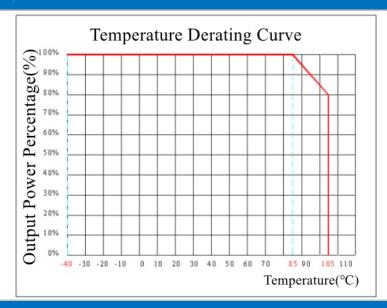
Item	Operatin	g Condition	Min.	Тур.	Max.	Unit	
Insulation Withstand	Input-output, Test 1min,		3000	-	-	VDC	
Voltage	leakage current≤0.5mA					VDC	
Insulation Resistance	Input-output, Insula	Input-output, Insulation Voltage 500VDC		-	-	ΜΩ	
Isolation Capacitor	Input-output, 100KHz/0.1V		-	20	-	PF	
Operating Temperature	Temperature≥105℃,see Temperature Derating Curve		-40	-	105		
Case Rising Temperature	Test Environment Temperature 25°C		-	15	-	°C	
Storage Temperature		-	-55	-	135		
Reflow Temperature	Peak Value Temperature 270°C °C≤Tc≤280°C, Only 1 time over Oven; Peak Value Temperature Tc≤270°C, only 3 times over Oven						
Storage Humidity	No condensing		-	-	95	%RH	
Switching Frequency	Full load	3.3Vdc/5Vdc Input	-	260	-	1211	
		12Vdc/24Vdc Input	-	450	-	KHz	
MTBF	MIL-HDBK-217F@25℃		3000			Khours	
Material Characteris	tics						
Case Material		Black flame-retardant heat-resistant plastic (UL94 V-0)					
Packing Dimension		12.7X11.20X7.25 mm					
Product Weight	SMD Package	1.4g (TYP.)					
Cooling M	lethod	Natural air cooling					
EMC Characteristic							
ЕМІ	CE	CISPR32/EN55032 CLASS B (See EMC Recommended Circuit below)					
	RE	CISPR32/EN55032 CLASS B (See EMC Recommended Circuit below)					
EMS	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±8kV perf. Criteria B					
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Product Character Curve



Application Circuit

1. Typical Application

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output side, application circuit as below photo 3; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance.



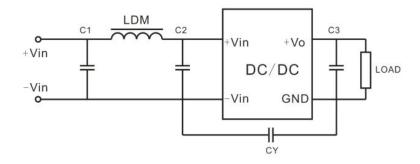
Note 1: Cin is 4.7uF/50V, Cout is 10uF/50V



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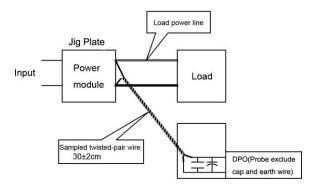
2. EMC Typical Recommended Circuit



Note 2:C1,C2 is 4.7uF/50V, LDM is 6.8uH, CY is 1nF/250Vac, for C3, please refer to the Typical Circuit.

- 3. Ripple & Noise Test((Twisted Pair Method 20MHZ bandwidth)
- 1).12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 4.7uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- 2). Ripple& Noise Test Method:

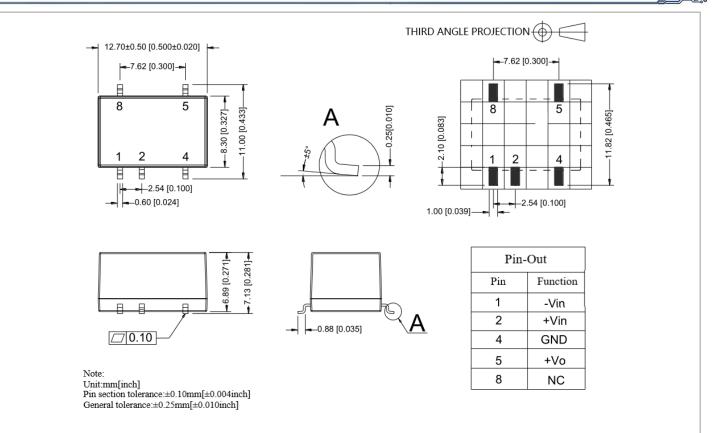
Input terminal connect to power supply, output terminal connect to electronic load through jig plate. Use 30cm±2 cm sampling line. Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Dimension

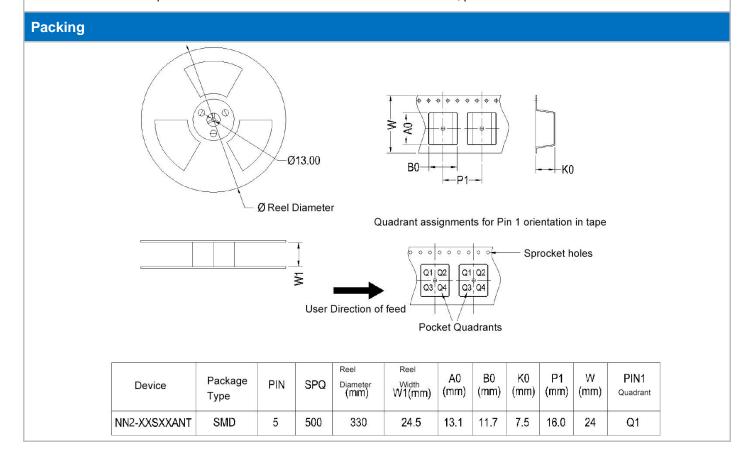


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NC pin:do not connect to any external circuit

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.





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- 1. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
- 2.The maximum capacitive load is tested under nominal input voltage range and full load condition;
- 3. Unless otherwise specified, data in this datasheet are tested under conditions of **Ta=25**℃, **humidity<75**% when inputting nominal voltage and outputting rated load(pure resistance load);
- 4. All index testing methods in this datasheet are based on our Company's corporate standards.
- 5. We can provide customized product service;